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OCT 30 2007

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1 Claims 1-25 (canceled)

1 Claim 26 (original): A method of making symbol timing  
2 adjustments in a communications device including a  
3 transmitter which transmits multiple symbols in each of a  
4 plurality of dwells, the method comprising the step of:  
5 determining the number of samples by which the symbol  
6 timing is to be advanced or delayed during a dwell;  
7 increasing the number of samples in one of a first  
8 symbol and a last symbol of said dwell by the determined  
9 number of samples when said symbol timing is to be delayed  
10 during said dwell by the determined number of samples; and  
11 decreasing the number of samples in one of the first  
12 symbol and the last symbol of said dwell by the determined  
13 number of samples when said symbol timing is to be advanced  
14 during said dwell by the determined number of samples.

1 Claim 27 (original): The method of claim 26, wherein the  
2 number of samples in the remaining symbols in the dwell  
3 which includes said one of the first symbol and the last  
4 symbol of said dwell to which samples were added or removed  
5 to adjust symbol timing do not have their number of symbols  
6 changed as part of making symbol timing adjustments.

1 Claim 28 (original): The method of claim 26,  
2 wherein said one of a first symbol and a last  
3 symbol of said dwell is said first symbol, the first symbol  
4 including a cyclic prefix portion and a body portion; and

5        wherein increasing the number of samples in said first  
6        symbol includes:  
7                copying samples from the body portion of  
8                said first symbol and inserting the copied  
9                samples at the start of said first symbol thereby  
10               increasing the number of samples in said first  
11               symbol.

1        Claim 29 (original): The method of claim 26,  
2                wherein said one of a first symbol and a last  
3        symbol of said dwell is said first symbol, the first symbol  
4        including a cyclic prefix portion and a body portion; and  
5                wherein decreasing the number of samples in said first  
6        symbol includes:  
7                removing samples from the start of the  
8                cyclic prefix portion thereby decreasing the  
9                number of samples in said first symbol.

1        Claim 30 (previously presented): The method of claim 26,  
2                wherein said one of a first symbol and a last  
3        symbol of said dwell is said last symbol, the last symbol  
4        including a cyclic prefix portion and a body portion; and  
5                wherein increasing the number of samples in said last  
6        symbol includes:  
7                copying samples from the body portion of said  
8                last symbol and inserting the copied samples at  
9                the end of said last symbol thereby increasing  
10               the number of samples in said last symbol.

1        Claim 31 (previously presented): The method of claim 26,  
2                wherein said one of a first symbol and a last symbol of  
3        said dwell is said last symbol; and  
4                wherein decreasing the number of samples in said  
5        last symbol includes:

6 removing samples from the end of said last symbol  
7 thereby decreasing the number of samples in  
8 said last symbol

1 Claim 32 (original): A communications device, comprising:  
2 a transmitter which transmits multiple symbols in each  
3 of a plurality of dwells, the transmitter including:  
4 means for determining the number of samples  
5 by which the symbol timing is to be advanced or  
6 delayed during a dwell;  
7 means for increasing the number of samples  
8 in one of a first symbol and a last symbol of  
9 said dwell by the determined number of samples  
10 when said symbol timing is to be delayed during  
11 said dwell by the determined number of samples;  
12 and  
13 means for decreasing the number of samples  
14 in one of the first symbol and the last symbol of  
15 said dwell by the determined number of samples  
16 when said symbol timing is to be advanced during  
17 said dwell by the determined number of samples.

1 Claim 33 (original): The device claim 32,  
2 wherein said one of a first symbol and a last  
3 symbol of said dwell is said first symbol, the first symbol  
4 including a cyclic prefix portion and a body portion; and  
5 wherein said means for increasing the number of  
6 samples in said first symbol includes:  
7 means for copying samples from the body  
8 portion of said first symbol and inserting the  
9 copied samples at the start of said first symbol  
10 to thereby increase the number of samples in said  
11 first symbol.

1 Claim 34 (original): The device of claim 32,  
2 wherein said one of a first symbol and a last  
3 symbol of said dwell is said first symbol, the first symbol  
4 including a cyclic prefix portion and a body portion; and  
5 wherein said means for decreasing the number of  
6 samples in said first symbol includes:  
7 means for removing samples from the start of  
8 the cyclic prefix portion to thereby decrease the  
9 number of samples in said first symbol.

1 Claim 35 (previously presented): The device of claim 32,  
2 wherein said one of a first symbol and a last  
3 symbol of said dwell is said last symbol, the last symbol  
4 including a cyclic prefix portion and a body portion; and  
5 wherein said means increasing the number of samples in  
6 said last symbol includes:  
7 means for copying samples from the body portion  
8 of said last symbol and inserting the copied  
9 samples at the end of said last symbol thereby  
10 increasing the number of samples in said last  
11 symbol.

1 Claim 36 (previously presented): The device of claim 35,  
2 wherein said one of a first symbol and a last symbol  
3 of said dwell is said last symbol; and  
4 wherein said means for decreasing the number of  
5 samples in said last symbol includes:  
6 means for removing samples from the end of  
7 said last symbol thereby decreasing the number of  
8 samples in said last symbol.

1 Claim 37 (currently amended): A method for adjusting  
2 symbol timing in a first communications device in a an

3 Orthogonal Frequency Division Multiplexing system, the  
4 method comprising:

5 determining a receiver symbol timing adjustment to be  
6 made to adjust receiver symbol timing in said first  
7 communications device to synchronize receiver symbol timing  
8 to the symbol timing of a second communications device; and  
9 adjusting the symbol timing of a transmitter in said  
10 first communications device as a function of said  
11 determined receiver symbol timing adjustment,

12 said step of adjusting the symbol timing of the  
13 transmitter including selecting one of a first and a last  
14 symbol in a dwell to be modified to adjust the transmitter  
15 symbol timing, said dwell being a period of time comprising  
16 multiple symbol ~~tones~~ times during which a tone or set of  
17 tones is used by the first communications device prior to  
18 switching to another tone or set of tones.

1 Claim 38 (previously presented): The method of claim 37,  
2 wherein said receiver symbol timing adjustment indicates  
3 that symbol timing should be adjusted by an amount  
4 corresponding to D digital signal samples.

1 Claim 39 (previously presented): The method of claim 38,  
2 wherein said step of determining a receiver symbol timing  
3 adjustment includes:  
4 receiving a symbol timing correction signal  
5 transmitted from said second communications device.

1 Claim 40 (currently amended): The method of claim 38,  
2 wherein the first communication device is a wireless  
3 terminal ~~(104, 106)~~.

1 Claim 41 (currently amended): The method of claim 40,  
2 wherein the second communication device is a base station  
3 ~~(102)~~.

1 Claim 42 (previously presented): The method of claim 41,  
2 further comprising:  
3 determining an additional receiver symbol timing  
4 adjustment to be made to adjust receiver symbol timing of  
5 an additional receiver in said first communications device  
6 to synchronize the additional receiver symbol timing to be  
7 the symbol timing of a third communications device, said  
8 third communications device being an additional base  
9 station; and  
10 adjusting the symbol timing of an additional  
11 transmitter in said first communications device as a  
12 function of said determined additional receiver symbol  
13 timing adjustment.

1 Claim 43 (previously presented): The method of claim 40,  
2 further comprising:  
3 adjusting the symbol timing of a receiver included in  
4 said first communications device to delay said receiver  
5 symbol timing by said D samples; and  
6 wherein the step of adjusting the symbol timing of  
7 said transmitter in said first communications device  
8 includes delaying the transmission of symbols by D samples  
9 by modifying the selected symbol by adding D samples to  
10 said selected symbol thereby increasing the duration of the  
11 selected symbol.

1 Claim 44 (previously presented): The method of claim 43,  
2 wherein symbols in said dwell other than said selected  
3 symbols are not changed as part of adjusting the symbol  
4 timing of said transmitter.

1 Claim 45 (previously presented): The method of claim 43,  
2 wherein the first symbol in said dwell is selected as said  
3 selected symbol, the selected symbol having N samples, the  
4 step of modifying the selected symbol by adding D samples  
5 including:  
6 copying D samples from a body of said first symbol and  
7 inserting the D copied samples at the start of said  
8 selected symbol to produce a modified first symbol having  
9 N+D samples.

1 Claim 46 (previously presented): The method of claim 43,  
2 wherein the last symbol in said dwell is selected as said  
3 selected symbol, the selected symbol having N samples, the  
4 step of adjusting the symbol timing further including:  
5 copying D samples from a body of said selected symbol  
6 and inserting the D copied samples at the end of said  
7 selected symbol to produce a modified last symbol having  
8 N+D.

1 Claim 47 (previously presented): The method of claim 40,  
2 wherein the step of adjusting the symbol timing of said  
3 transmitter in said wireless terminal includes:  
4 adjusting the symbol timing of said transmitter  
5 included in said first communications device to advance the  
6 transmission of symbols.

1 Claim 48 (previously presented): The method of claim 47,  
2 wherein advancing the transmission of symbols includes the  
3 step of removing D samples from said selected symbol  
4 thereby decreasing the duration of said selected symbol.

1 Claim 49 (previously presented): The method of claim 48,  
2 wherein said selected symbol is the first symbol in said



3 dwell, the selected symbol includes N samples beginning  
4 with a K sample cyclic prefix; and  
5 wherein adjusting the symbol timing of said  
6 transmitter includes modifying said selected symbol by  
7 deleting D samples from the start of the K sample cyclic  
8 prefix of said selected symbol to produce a first modified  
9 symbol having N-D samples, where N, D and K are positive  
10 non-zero integers.

1 Claim 50 (previously presented): The method of claim 48,  
2 wherein said selected symbol is the last symbol in said  
3 dwell, the selected last symbol having N samples; and  
4 wherein adjusting the symbol timing of said  
5 transmitter includes modifying said selected symbol by  
6 deleting D samples from the end of said selected symbol to  
7 produce a modified last symbol having N-D samples, where N  
8 and D are positive non-zero integers.

1 Claim 51 (currently amended): A mobile communications  
2 device for an Orthogonal Frequency Division Multiplexing  
3 system, comprising:  
4 a clock;  
5 receiver symbol timing control circuitry (208) coupled  
6 to said clock ~~(210)~~ for determining a receiver symbol  
7 timing adjustment used to synchronize receiver symbol  
8 timing to the symbol timing of at least one broadcast  
9 signal;  
10 transmitter symbol timing control circuitry coupled to  
11 said clock and to said receiver symbol timing control  
12 circuitry, the transmitter symbol timing control circuitry  
13 receiving symbol timing adjustment information from said  
14 receiver symbol timing adjustment circuitry  
15 said transmitter symbol timing adjustment making a  
16 transmitter symbol timing adjustment in a direction which



17 is the same as a receiver symbol timing adjustment made by  
18 said receiver symbol timing control circuitry;  
19 said transmitter timing control circuitry including  
20 means for selecting a symbol to be lengthened or shortened  
21 prior to transmission to implement said symbol timing  
22 adjustment, said selected symbol being one of a first  
23 symbol and a last symbol in a dwell, said dwell being a  
24 period of time during which a transmitter of said mobile  
25 communications device remains on the same tone or being a  
26 period of time comprising multiple symbol tones on which  
27 said mobile communications device remains prior to  
28 switching to another tone or set of tones.

1 Claim 52 (previously presented): The mobile communications  
2 device of claim 51, wherein the symbol timing control  
3 circuitry further includes:

4 copy circuitry for performing a cyclic copy to be  
5 added samples to said selected symbol to be transmitted  
6 when said transmitter symbol timing is to be delayed; and  
7 deleting circuitry for deleting samples from said  
8 selected symbol to be transmitted when said transmitter  
9 symbol timing is to be advanced.

1 Claim 53 (previously presented): The mobile communications  
2 device of claim 52, wherein said symbols are frequency  
3 division multiplexed symbols, the mobile communication  
4 device further comprising:

5 an antenna for transmitting symbols including a symbol  
6 whose duration has been changed by one of said copy  
7 circuitry and said deleting circuitry.

1 Claim 54 (currently amended): The mobile communications  
2 device of claim 51,

3 wherein said receiver symbol timing control circuitry  
4 ~~(208)~~ includes means for independently determining symbol  
5 timing adjustments to be made when processing symbols  
6 corresponding to each of a first and a second base station;  
7 and

8 wherein said transmitter symbol timing control  
9 circuitry ~~(212)~~ includes means for independently adjusting  
10 the symbol timing of symbols transmitted to the first and  
11 second base station, respectively, as a function of the  
12 symbol timing adjustments determined to be made when  
13 processing symbols corresponding to the first and second  
14 base stations, respectively.

1 Claim 55 (new): A device comprising a processor configured  
2 to control the device to implement a method of making  
3 symbol timing adjustments in the device, the method  
4 comprising the steps of:

5 determining the number of samples by which the symbol  
6 timing is to be advanced or delayed during a dwell;  
7 increasing the number of samples in one of a first  
8 symbol and a last symbol of said dwell by the determined  
9 number of samples when said symbol timing is to be delayed  
10 during said dwell by the determined number of samples; and  
11 decreasing the number of samples in one of the first  
12 symbol and the last symbol of said dwell by the determined  
13 number of samples when said symbol timing is to be advanced  
14 during said dwell by the determined number of samples.

1 Claim 56 (new): The device of claim 55, wherein the number  
2 of samples in the remaining symbols in the dwell which  
3 includes said one of the first symbol and the last symbol  
4 of said dwell to which samples were added or removed to  
5 adjust symbol timing do not have their number of symbols  
6 changed as part of making symbol timing adjustments.

1 Claim 57 (new): The device of claim 55,  
2 wherein said one of a first symbol and a last symbol  
3 of said dwell is said first symbol, the first symbol  
4 including a cyclic prefix portion and a body portion; and  
5 wherein increasing the number of samples in said first  
6 symbol includes:  
7 copying samples from the body portion of said  
8 first symbol and inserting the copied samples at the  
9 start of said first symbol thereby increasing the  
10 number of samples in said first symbol.

1 Claim 58 (new): A computer readable medium embodying  
2 machine executable instructions for controlling a  
3 communications device to implement a method of making  
4 symbol timing adjustments in the communications device, the  
5 method comprising the steps of:  
6 determining the number of samples by which the symbol  
7 timing is to be advanced or delayed during a dwell;  
8 increasing the number of samples in one of a first  
9 symbol and a last symbol of said dwell by the determined  
10 number of samples when said symbol timing is to be delayed  
11 during said dwell by the determined number of samples; and  
12 decreasing the number of samples in one of the first  
13 symbol and the last symbol of said dwell by the determined  
14 number of samples when said symbol timing is to be advanced  
15 during said dwell by the determined number of samples.

1 Claim 59 (new): The computer readable medium of claim 58,  
2 wherein the number of samples in the remaining symbols in  
3 the dwell which includes said one of the first symbol and  
4 the last symbol of said dwell to which samples were added  
5 or removed to adjust symbol timing do not have their number  
6 of symbols changed as part of making symbol timing  
7 adjustments.

1 Claim 60 (new): The computer readable medium of claim 58,  
2 wherein said one of a first symbol and a last symbol  
3 of said dwell is said first symbol, the first symbol  
4 including a cyclic prefix portion and a body portion; and  
5 wherein increasing the number of samples in said first  
6 symbol includes:  
7 copying samples from the body portion of  
8 said first symbol and inserting the copied  
9 samples at the start of said first symbol thereby  
10 increasing the number of samples in said first  
11 symbol.

1 Claim 61 (new): A computer readable medium embodying  
2 machine executable instructions for controlling a first  
3 communications device to implement a method of adjusting  
4 symbol timing, the method comprising:  
5 determining a receiver symbol timing adjustment to be  
6 made to adjust receiver symbol timing in said first  
7 communications device to synchronize receiver symbol timing  
8 to the symbol timing of a second communications device; and  
9 adjusting the symbol timing of a transmitter in said  
10 first communications device as a function of said  
11 determined receiver symbol timing adjustment,  
12 said step of adjusting the symbol timing of the  
13 transmitter including selecting one of a first and a last  
14 symbol in a dwell to be modified to adjust the transmitter  
15 symbol timing, said dwell being a period of time comprising  
16 multiple symbol times during which a tone or set of tones  
17 is used by the first communications device prior to  
18 switching to another tone or set of tones.

1 Claim 62 (new): The computer readable medium of claim 61,  
2 wherein said receiver symbol timing adjustment indicates

3 that symbol timing should be adjusted by an amount  
4 corresponding to D digital signal samples.

1 Claim 63 (new): The computer readable medium of claim 62,  
2 wherein said step of determining a receiver symbol timing  
3 adjustment includes:

4 receiving a symbol timing correction signal  
5 transmitted from said second communications device.

1 Claim 64 (new): A processor configured to control a first  
2 communications device to implement a method of adjusting  
3 symbol timing, the method comprising:

4 determining a receiver symbol timing adjustment to be  
5 made to adjust receiver symbol timing in said first  
6 communications device to synchronize receiver symbol timing  
7 to the symbol timing of a second communications device; and  
8 adjusting the symbol timing of a transmitter in said  
9 first communications device as a function of said  
10 determined receiver symbol timing adjustment,

11 said step of adjusting the symbol timing of the  
12 transmitter including selecting one of a first and a last  
13 symbol in a dwell to be modified to adjust the transmitter  
14 symbol timing, said dwell being a period of time comprising  
15 multiple symbol times during which a tone or set of tones  
16 is used by the first communications device prior to  
17 switching to another tone or set of tones.

1 Claim 65 (new): The processor of claim 64, wherein said  
2 receiver symbol timing adjustment indicates that symbol  
3 timing should be adjusted by an amount corresponding to D  
4 digital signal samples.

1 Claim 66 (new): The processor of claim 65, wherein said  
2 step of determining a receiver symbol timing adjustment  
3 includes:  
4       receiving a symbol timing correction signal  
5 transmitted from said second communications device.